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APPLICATION NO.	APPLICATION NO. FILING DATE		FIRST NAMED INVENTOR		ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/786,296	02/26/2004	Nobuhiro		lishikawa	103213-00072	3437	
TITLE OF INVENTION: S MOS TRANSISTOR	EMICONDUCTOR INTEG	RATED CIRCUIT I	EVICE CO	NFIGURED TO PREVENT	THE GENERATION OF A	REVERSE CURRENT IN A	
APPLN. TYPE	SMALL ENTITY	ISSUE FEE		PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE	
nonprovisional	NO	\$1400		\$300	\$1700	10/27/2005	
EXAMINER AR		ART UNII		CLASS-SUBCLASS			
NADAV, ORI 28				257-355000	_		
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DI DAGE MOTE, Unles	s an assignee is identified b in 37 CFR 3.11. Completion	elow, no assignee d	ata will app	ear on the patent. If an ass	ignee is identified below, the	document has been filed for	
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Rohm Co., Ltd.			Kyoto, Japan		01 FC:1501 02 FC:1504 03 FC:8001	1400.00 300.00 30.00	
	te assignee category or category	ories (will not be prin	ited on the p		Corporation or other private	group entity Cardovernment	
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Applicant claims	s (from status indicated above SMALL ENTITY status. Seg	/37 CFR 1.27.	b. Applic	cant is no longer claiming SM	MALL ENTITY status. See 37	CFR 1.27(g)(2).	
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Typed or printed name Sam Huano)_				
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